

S6	21	S5 and RF	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:01
S7	21	S6 and port	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:00
S8	21	S7 and processor	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:43
S9	0	S8 and "housing frame"	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:01
S10	19	S8 and frame	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 16:43
S11	21	S8 and coaxial	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 16:43
S12	19	S10 and coaxial	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:01
S13	0	S1 and (multidirection near3 tap)	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 16:47
S14	502	S1 and (cable or "cable TV" or CATV)	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:00
S15	27	S14 and (revers\$ adj signal)	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:00
S16	0	S15 and "I/O"	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:24
S17	19	S15 and port	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:25
S18	1	S17 and "housing frame"	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:25
S19	19	S17 and RF	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:25
S20	17	S17 and RF	USPAT	OR	ON	2004/04/02 17:01
S21	14	S20 and coaxial	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:02
S22	2377	(signal adj tap) or (cable adj tap)	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:24

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	144	reversible near5 (tap or coupler)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:44
L2	5	L1 and CATv	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:44
L3	3	L1 and (cover near9 switch\$3)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:44
L4	857	(signal near5 tap) and (revers\$3 near5 direction)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:44
L5	95	L4 and CATv	USPAT	OR	ON	2005/10/07 17:44
L6	23	L5 and "I/O"	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:44
L7	23	L6 and port	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:44
L8	23	L7 and (output and input)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:44
L9	23	L8 and RF	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:44
S1	811	(signal near5 tap) and (revers\$3 near5 direction)	US-PGPUB; USPAT; EPO	OR	ON	2005/01/03 16:18
S2	120	S1 and CATv	US-PGPUB; USPAT; EPO	OR	ON	2005/01/03 16:22
S3	0	S2 and "directional processor"	US-PGPUB; USPAT; EPO	OR	ON	2005/01/03 16:19
S4	2	S2 and "directional component"	US-PGPUB; USPAT; EPO	OR	ON	2005/01/03 16:20
S5	94	S1 and CATv	USPAT	OR	ON	2005/01/03 16:22
S6	23	S5 and "I/O"	US-PGPUB; USPAT; EPO	OR	ON	2005/01/03 16:24
S7	23	S6 and port	US-PGPUB; USPAT; EPO	OR	ON	2005/01/03 16:24

S8	0	S7 and ("output port" and "input port")	US-PGPUB; USPAT; EPO	OR	ON	2005/01/03 16:24
S9	23	S7 and (output and input)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/06 17:08
S10	23	S9 and RF	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:44
S11	1	("5638035").PN.	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/06 17:09
S12	144	reversible near5 (tap or coupler)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/06 17:16
S13	0	S12 and "housing frame"	US-PGPUB; USPAT; EPO	OR	ON	2005/10/06 17:11
S14	0	S12 and (cover near9 circuitry)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/06 17:11
S15	3	S12 and (cover near9 switch\$3)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:44
S16	5	S12 and CATv	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:44
S17	0	S16 and (reversible near5 top)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/06 17:16
S18	0	S16 and (reversible near5 cover)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/06 17:16
S19	0	S16 and (housing near8 reversible)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/06 17:17
S20	0	S16 and (frame near8 reversible)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/06 17:17
S21	0	S16 and (top near8 reversible)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/06 17:17
S22	0	S16 and (top near8 revers\$5)	US-PGPUB; USPAT; EPO	OR	ON	2005/10/06 17:17
S23	1	("6133939").PN.	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/06 17:27

S23	36	S22 and reversible	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:42
S24	1	S23 and "I/O"	US-PGPUB; USPAT; EPO	OR	ON	2005/10/07 17:42
S25	14	S23 and port	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:25
S26	8	S25 and RF	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:26
S27	0	S26 and "housing frame"	US-PGPUB; USPAT; EPO	OR	ON	2004/04/02 17:25